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				Application Number	09/359,056-Conf. #2286
				Filing Date	July 21, 1999
				First Named Inventor	Barmak Mansoorian
				Art Unit	2615
				Examiner Name	N. T. Tran
Sheet	1	of	1	Attorney Docket Number	M4065.0767/P767

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS						
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mt	CA	H.B. Bakoglu, Circuits, Interconnections, and Packaging for VLSI, Addison-Wesley VLSI Systems Series, 1990.	

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